

Brief Introduction to Abhijit R Abhyankar

Abhijit R. Abhyankar (PhD) is a NTPC Chair Professor of Electrical Engineering Department at Indian Institute of Technology Delhi, New Delhi, India. Currently, he is also a member of Global Future Council on Clean Electrification of World Economic Forum.

He has acted as expert member of various committees established by the Central Electricity Regulatory Commission (CERC) to provide technical support to resolve regulatory issues. He is member of National Reliability Council for Electricity (NRCE), set-up by Ministry of Power, Government of India, through Central Electricity Authority (CEA). He is also a member of Taskforce on PoC Transmission Pricing Review, constituted by Central Electricity Regulatory Commission (CERC), New Delhi.

He has handled important projects and done consultancies for various agencies like POWERGRID, Maharashtra Electricity Regulatory Commission, National High Speed Railway Corporation, Department of Science & Technology, Indian Energy exchange, Mercaods EMI Pvt. Ltd. and State Electricity Utilities.

He has more than hundred papers published in various International journals, International conferences and National conferences to his credit. He has developed one web-based course titled – “Restructured Power Systems” which is available on NPTEL website (<http://nptel.iitm.ac.in/courses/108101005/>).

His current research interests: Power System Analysis and optimization, Power Markets, Distribution System Operator (DSO), Power Industry Policy and regulatory matters, Smart Grids, Transmission System Flexibility.