

Anurag K. Srivastava is a Raymond J. Lane Professor and Chairperson of the Computer Science and Electrical Engineering Department at the West Virginia University. He is also an adjunct professor at the Washington State University and senior scientist at the Pacific Northwest National Lab. He received his Ph.D. degree in electrical engineering from the Illinois Institute of Technology in 2005. His research interest includes data-driven algorithms for power system operation and control including resiliency analysis. In past years, he has worked in a different capacity at the Réseau de transport d'électricité in France; RWTH Aachen University in Germany; PEAK Reliability Coordinator, Idaho National Laboratory, PJM Interconnection, Schweitzer Engineering Lab (SEL), GE Grid Solutions, Massachusetts Institute of Technology and Mississippi State University in USA. He is serving as vice-chair of the IEEE Power & Energy Society's (PES) of power system operation SC, chair of PES voltage stability working group, chair of PES synchrophasors applications working group, co-chair of distributed optimization application in power grid, co-chair of the microgrid working group, vice-chair of tools for power grid resilience TF. He is an IEEE Fellow, IEEE Distinguished lecturer and the author of more than 300 technical publications including a book and 3 patents.